

Ake

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	10/733,216	TU ET AL.
	Examiner	Art Unit
	Tu-Tu Ho	2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to Papers filed 12/11/2003.
2.  The allowed claim(s) is/are 1-21.
3.  The drawings filed on 11 December 2003 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 01/29/2004
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

  
 David Nelms  
 Supervisory Patent Examiner  
 Technology Center 2800

## **DETAILED ACTION**

### *Allowable Subject Matter*

1. Claims 1-21 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: The prior art of record fails to teach or render obvious a memory cell and a method of manufacturing thereof having all exclusive limitations as recited in claims 1, 7, and 8, comprising a capacitor having two electrodes, a lining over the capacitor, a spacer, and a gate electrode extending over the capacitor, characterized in either that:

the spacer has a first non-planer profile configured to engage a second non-planar profile comprising ends of one of the electrodes and the lining, the electrodes are vertically disposed electrodes, the lining is insulating, and the spacer isolates an end of the one of the electrodes from an extension of the gate electrode or the gate electrode couples to the capacitor; or that:

the electrodes are formed in an opening in a substrate, ends of one of the electrodes extend over and are parallel to the substrate, the spacer isolates one end of the one of the electrodes from an extension of the gate electrode, and the spacer includes a protrusion conforming to a recess defined by and end of the lining ends and the one end of the one of the electrodes.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

Art Unit: 2818

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Conclusion***

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Copies of the references are not being furnished with this Office Action per MPEP § 707.05(a).

a) U.S. Patent 6,661,050 to Tzeng et al. discloses a 1T-1C memory cell having a trench capacitor and a transistor planar with the capacitor.

b) U.S. Patent 5,950, 084 to Chao discloses a DRAM cell having a dual-packed trench capacitor and a transistor planar with the capacitor.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR

Art Unit: 2818

system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TH

Tu-Tu Ho  
September 04, 2004



David Nelms  
Supervisory Patent Examiner  
Technology Center 2800